



IPW

<b>AMENDMENT TRANSMITTAL LETTER</b>			Docket No. SON-2999		
Application No. 10/827,351 Conf. No. 2615	Filing Date April 20, 2004	Examiner H.N. Nguyen	Art Unit 2824		
Applicant(s): Kou Nagata					
Invention: SEMICONDUCTOR MEMORY APPARATUS AND SELF-REPAIR METHOD					
<b>TO THE COMMISSIONER FOR PATENTS</b>					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
<b>CLAIMS AS AMENDED</b>					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	13	- 20 =		x	
Independent Claims	2	- 3 =		x	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:</b>					0.00
<input checked="" type="checkbox"/> Large Entity <span style="margin-left: 200px;"><input type="checkbox"/> Small Entity</span>					
<input checked="" type="checkbox"/> No additional fee is required for this amendment.					
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ _____. A duplicate copy of this sheet is enclosed.					
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <u>18-0013</u> as described below. A duplicate copy of this sheet is enclosed.					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.					
Ronald P. Karanah Attorney Reg. No.: 24,104  RADER, FISHMAN & GRAUER PLLC 1233 20th Street, N.W. Suite 501 Washington, DC 20036 (202) 955-3750					Dated: <u>May 19, 2005</u>



Docket No.: SON-2999  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Kou Nagata

Application No.: 10/827,351

Confirmation No.: 2615

Filed: April 20, 2004

Art Unit: 2824

For: SEMICONDUCTOR MEMORY APPARATUS  
AND SELF-REPAIR METHOD

Examiner: H. N. Nguyen

**AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated March 30, 2005 (Paper No./Mail Date 20050318),  
please amend the above-identified U.S. patent application as follows: